ANALOG INTERFACE STRUCTURES AND METHODS
FOR DIGITAL DISPLAYS

ABSTRACT OF THE DISCLOSURE

Structures and methods are provided for generating a digital display signal from an analog signal that is limited to 2^N discrete

5   analog levels and from a synchronization signal that defines spatial order for the digital display signal. These structures and methods accurately synchronize digitizers to the analog signal and they follow from a recognition that enhanced digitizer resolution will generate code patterns which easily distinguish between correct and incorrect

10   sampling of the analog signals. Accordingly, the digitizers quantize the analog samples into an M-bit digital display signal wherein M exceeds N.